

Device Biasing, Trigger Function, and Timing

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(Source Section 9)

9.1 Diagram for TR Imaging — SanjCONTROLLER PLUS™ signals:

- **A. VI EXT:** Input for fixed DC voltage up to +60V.
- **B. VO EXT:** Pulsed output of the DC voltage applied to VI EXT.
- **C. AUX INT:** Up to $\pm 10\text{V}$ DC for secondary DUT bias.
- **D. VO INT:** Up to $\pm 10\text{V}$ pulse for DUT pulsed bias.
- **E. TRIG:** Internal-mode output, 5V TTL pulse, syncs an external DUT bias supply.

9.2 Relation to Timing Sequence for Transient Analysis. [diagram/image in source — not reproduced here]

The following subsections consist of diagrams in the source with no accompanying body text — content not reproduced here:

- **9.3 Internal Biasing** [diagram/image in source — not reproduced here]
- **9.4 External DC Supply** [diagram/image in source — not reproduced here]
- **9.5 Triggering an External Source** [diagram/image in source — not reproduced here]
- **9.6 Slave Mode (TRIG IN) in Movie Mode only** [diagram/image in source — not reproduced here]
- **9.7 Transient Timing Signal** [diagram/image in source — not reproduced here]
- **9.8 Transient Software with Light Emission** [diagram/image in source — not reproduced here]