

10. Device Biasing, Trigger Function, and Timing Reference

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SanjCONTROLLER PLUS™ I/O (for TR imaging):

Label	Description
A. VI EXT	Input for fixed DC voltage up to +60V
B. VO EXT	Pulsed output of DC voltage input to VI EXT
C. AUX INT	Up to ±10V DC for secondary DUT bias
D. VO INT	Up to ±10V pulse for DUT pulsed bias
E. TRIG	Internal mode output; 5V TTL pulse to sync an external DUT bias supply

[diagram/image in source — not reproduced here: SanjCONTROLLER PLUS diagram for TR imaging]

The remaining topics in this source chapter are presented only as diagrams/screenshots, with no accompanying procedural text: Relation to Timing Sequence for Transient Analysis; Internal Biasing; External DC Supply; Triggering an External Source; Slave Mode (TRIG IN) in Movie Mode only; Transient Timing Signal; Transient Software with Light Emission. [diagram/image in source — not reproduced here, for all of the above]